



JPW
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Chen

Serial No.: 10/780,932

Filed: February 17, 2004

For: Interconnect Structure for TFT-
Array Substrate and Method for
Fabricating the Same

)
)
) Group Art Unit: 2815

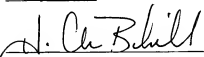
)
) Examiner: Chu, Chris C.

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) Confirmation No. 1914

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) TKHR Docket No.: 250122-1220
) Top-Team Ref. 0632-A50054US

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to: Mail Stop Amendment; Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on January 18, 2005.


Signature – Hui Chin Barnhill

AMENDMENT AND RESPONSE TO RESTRICTION REQUIREMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

The Office Action mailed January 4, 2004, has been carefully considered. In response thereto, please enter the following amendments and consider the following remarks.